# A Color LED Driver Implemented by the Active Clamp Forward Converter 

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#### Abstract

Because light emitting diodes (LEDs) have the advantages of dc working voltage, high luminescent efficiency, short ignition time, high reliability and pollution free, they have substituted for incandescent bulbs and fluorescent lamps gradually. In order to simplify circuit complexity, an active clamp forward converter with the sequential color display (SCD) control is proposed to drive red, green and blue (RGB) LED arrays. The proposed converter has zero-voltage switching (ZVS) operations of both the main switch and the auxiliary switch, resulting in high system efficiency. Driving RGB LED arrays sequentially by one converter can save components and reduce cost significantly. Additionally, the pulse-width modulation (PWM) control is applied to achieve a large chromaticity variation. The circuit operations are analyzed in detail and the circuit parameters are designed based on the practical considerations. Finally, an illustrative example is implemented to demonstrate the feasibility and validity of the proposed LED driver.


Keywords: Light emitting diode (LED), active clamp forward converter, sequential color display (SCD).

## 1. Introduction

Recently, light emitting diodes (LEDs) have substituted for incandescent bulbs and fluorescent lamps gradually in the residential, industrial and commercial lighting applications [1], because they have the advantages of dc working voltage, high luminescent efficiency, short ignition time, high reliability and pollution free, etc. Moreover, since mixing multi-coloured LEDs can provide a large chromaticity variation, LEDs are progressively used in outdoor landscape lighting systems as well.

Figure 1 shows the block diagram of a photovoltaic powered landscape lighting system, in which an isolated DC/DC converter is used to fulfill a stepdown conversion, govern the current through LED arrays and provide the electrical isolation of safety requirements. Generally, a colored LED module is composed of red, green and blue (RGB) LED arrays. Since RGB LEDs have different electrical characteristics, three isolated DC/DC converters are required to regulate the current through each LED arrays [2]. Therefore, the number of power components will become triple, resulting in large volume, high cost and low reliability.


Figure 1. The block diagram of a photovoltaic powered landscape lighting system.

In the literature [3, 4], a sequential color scanning scheme was proposed to reduce power consumption. LEDs are very suitable for a sequential color display (SCD) due to their fast response. When LEDs are displayed intermittently with high frequency, human cannot observe the darkness of short time intervals. The SCD control has been applied in a color LED backlight driving system for liquid crystal display (LCD) panels [5], [6], in which RGB LED arrays are sequentially driven by the same DC/DC converter.

Many isolated DC/DC converters for RGB LED driving, such as flyback and forward converters, have been presented in the literature [3, 5].

However, their hard-switching operations reduce conversion efficiency and induce electromagnetic interference (EMI) noise. This paper proposes an active clamp forward converter [7-9] with SCD control as the color LED driver for a photovoltaic powered landscape lighting system. For this driver, due to saving transformers, power switches, control ICs and energy storage components, the circuit complexity can be simplified, and the system volume and cost can be significantly reduced. With accurate analysis and design, both of the main switch and the auxiliary switch can turn on under zero-voltage switching (ZVS), leading to higher system efficiency and lower EMI noise. Besides, the pulse-width modulation (PWM) control is applied to change the luminance of RGB LEDs so that a large chromaticity variation can be achieved. The operation principles are addressed. Experimental results of a 100 W laboratory prototype are used to verify the feasibility and validity of the theoretical predictions.

## 2. Operation principles

The proposed color LED driver, as shown in figure 2, consists of an active clamp forward converter and RGB LED arrays with SCD control. In this section, the operation principles of the SCD control and the proposed converter are introduced briefly in the following.

### 2.1 Sequential color display control

To achieve SCD control, RGB LED arrays are connected in series with the switches $S_{R}, S_{G}$ and $S_{B}$, respectively. The switches are driven with phase-shift manner, as illustrated in figure 3, in which $v_{G S_{-} R}, v_{G S_{-} G}$ and $v_{G S_{-} B}$ are their gate signals with the switching frequency $f_{m}$. The frequency of the driving signals $v_{G S 1}$ and $v_{G S 2}$ in the active clamp forward converter is $f_{s}$ which should be much higher than $f_{m}$. The voltages $V_{O_{-} R}, V_{O_{-} G}$ and $V_{O_{-} B}$ are the forward voltage of RGB LED arrays, respectively. The currents $i_{R}, i_{G}$ and $i_{B}$ are their corresponding driving currents.

Assuming that the currents $i_{R}, i_{G}$ and $i_{B}$ have the same amplitude $I_{P}$ at full-load condition, the maximum average output-current $l_{o, a v g}$ of a conventional RGB LED driver is $3 I_{P}$. For the proposed driver, if the maximum duty cycle of the driving signals $v_{G S_{-} R}, v_{G S_{-} G}$ and $v_{G S_{-} B}$ are $D_{\text {max }}$, then the maximum average output-current $I_{0, a v g}$ is
$I_{o, a v g}=I_{P} \times D_{\max } \times 3$
Because $D_{\max }$ is $33.33 \%$ in the proposed driver, $I_{o, a v g}$ is only equal to $I_{p}$. Therefore, the required output-power of the proposed driver is only onethird of that of a conventional driver, which results in energy saving significantly.


Figure 2. The conceptual circuit of the proposed color LED driver with SCD control.


Figure 3. Driving signals and conceptual key waveforms of the SCD control.

In the proposed system, the red, green and blue LED arrays are driven by the same converter sequentially, leading to small volume, low cost and high reliability. All of the driving signals $v_{G S_{R}}, v_{G S_{-G}}$ and $v_{G S_{-} B}$ are produced by the digital signal processor (dsPIC30F4011). The luminance of each LED array is dimmable by modulating the duty of its driving signal, so as the color of the RGB-LED module is variable. The output capacitor $C_{o}$ with low capacitance is used in the converter so that the output voltage can vary promptly according to the forward voltage of each LED array.

### 2.2 Active clamp forward converter

Figure 4 shows the circuit diagram of the active clamp forward converter. Each active switch $S_{1}$ $\left(S_{2}\right)$ is composed of an MOSFET $Q_{1}\left(Q_{2}\right)$ and its intrinsic anti-parallel diode $D_{1}\left(D_{2}\right)$. The forward converter is formed by the main switch $S_{1}$, transformer $T_{x}$, forward diode $D_{3}$, freewheel diode $D_{4}$, output inductor $L_{0}$, and output capacitor $C_{o}$. Instead of the reset winding in traditional forward converter, the auxiliary switch $S_{2}$ and the clamping
capacitor $C_{c}$ are used to reset the residual flux of transformer. The residual energy of magnetizing inductor $L_{m}$ can be restored to the input voltage source. Both of the main switch $S_{1}$ and the auxiliary switch $S_{2}$ can turn on under ZVS, resulting in low switching losses and high system efficiency.

For simplifying the analysis of operation principles, following conditions are made:

1) All components are ideal.
2) The capacitance of $C_{c}$ is much higher than that of the intrinsic capacitor $C_{d s}$, and the inductance of $L_{m}$ is much higher than that of the leakage inductor $L_{r}$.
3) $L_{o}$ and $C_{o}$ are large enough so that the output voltage $V_{0}$ and output current $I_{0}$ can be regarded as constant.
4) The dead time between the driving signals of $S_{1}$ and $S_{2}$ is very short and negligible. The turn-on intervals of $S_{1}$ and $S_{2}$ are considering as $D T_{s}$ and $(1-D) T_{s}$, in which $T_{s}$ is the switching period in steady-state operation.

According to the volt-second balance concept, the relation of input voltage $V_{B}$ and the voltage across clamping capacitor $v_{c}$ can be expressed as
$V_{B} D T_{s}=\left(v_{c}-V_{B}\right)(1-D) T_{s}$
Arranging Eq. 2 yields
$v_{c}=\frac{1}{(1-D)} V_{B}$


Figure 4. Circuit diagram of the active clamp forward converter.

Eq. 3 indicates that $v_{c}$ is the output of boost-type operation with input voltage $V_{B}$. When $S_{1}$ turns off and $S_{2}$ turns on, the voltage across main switch $v_{d s 1}$ will be equal to $v_{c}$ and determined as
$v_{d s 1}=v_{c}=\frac{1}{(1-D)} V_{B}$
Figure 5 shows the theoretical waveforms of the active clamp forward converter, which is divided into nine operation modes within one switching period. The operation principle of each mode will be introduced referring to the equivalent circuits shown in figure 6.

Mode $1\left(t_{0} \sim t_{1}\right)$ : At the moment $t=t_{0}$, the switch $S_{1}$ is turning on, and the switch $S_{2}$ is turning off. Since the voltage across magnetizing inductor $v_{n 1}$ is equal to $V_{B}$, its current $i_{L m}$ increases linearly. The voltage across secondary winding $v_{n 2}$ is $V_{B} / n$ so that the forward diode $D_{3}$ turns on and the freewheel diode $D_{4}$ turns off. The input voltage source charges output inductor $L_{o}$ through the transformer $T_{x}$ and $D_{3}$. This mode ends while the gate signal of $S_{1}$ disappears.


Figure 5. Theoretical waveforms of the active clamp forward converter.

Mode $2\left(t_{1} \sim t_{2}\right)$ : At the moment $t=t_{1}$, the main switch $S_{1}$ turns off, and the current $i_{L r}$ charges the intrinsic capacitor $C_{d s}$ of main switch. The voltage $v_{d s 1}$ increases, and the voltage $v_{n 1}$ decreases. This mode ends while $V_{d s 1}$ reaches $V_{B}$.

Mode $3\left(t_{2} \sim t_{3}\right)$ : At the moment $t=t_{2}$, the voltage $v_{n 1}$ reaches zero so that $D_{3}$ and $D_{4}$ are both turnon. Since the diode current $i_{D 3}$ decreases, the leakage inductor current $i_{L r}$ decreases as well. The magnetizing inductor current $i_{L m}$ remains the same. This mode ends while $V_{d s 1}$ reaches $V_{B} /(1-D)$.

Mode $4\left(t_{3} \sim t_{4}\right)$ : At the moment $t=t_{3}$, the main switch $S_{1}$ turns off. The current $i_{L r}$ flows through anti-paralleled diode $D_{2}$ to transfer energy to the clamping capacitor $C_{c}$, which provides ZVS operation for the auxiliary switch $S_{2}$ turn-on. Gate signal of $S_{2}$ should be applied during this mode. This mode ends while $i_{D 3}$ reaches zero.

Mode $5\left(t_{4} \sim t_{5}\right)$ : At the moment $t=t_{4}$, because the current $i_{D 3}$ is zero, the inductor $L_{m}$ is in series with $L_{r}$ and participates in the resonance with $C_{c}$. The voltage $v_{n 1}$ is $D V_{B}$ / (1-D) with reverse polarity. The resonant current $i_{L r}$ is equal to the current $i_{L m}$ and remains decreasing. While $i_{L r}$ becomes negative during this mode, the energy of $L_{m}$ will be restored to the input voltage source. This mode ends while the gate signal of $S_{2}$ disappears.

Mode $6\left(t_{5} \sim t_{6}\right)$ : At the moment $t=t_{5}$, the auxiliary switch $S_{2}$ turns off, and the resonant current $i_{L r}$ follows through the intrinsic capacitor $C_{d s}$ to discharge it. This mode ends while the voltage $v_{d s 1}$ decreases to $V_{B}$.

Mode $7\left(\mathrm{t}_{6} \sim \mathrm{t}_{7}\right)$ : At the moment $\mathrm{t}=\mathrm{t}_{6}$, the voltage $v_{d s 1}$ is equal to $V_{B}$ so that the voltage $v_{n 1}$ becomes zero again. Since $D_{3}$ are turning on now, its current $i_{D 3}$ increases and the current $i_{L r}$ increases as well. The magnetizing inductor current $i_{L m}$ remains the same. This mode ends while $v_{d s 1}$ reaches zero.

Mode $8\left(\mathrm{t}_{7} \sim \mathrm{t}_{8}\right)$ : At the moment $\mathrm{t}=\mathrm{t}_{7}$, the current $i_{L_{r}}$ flows through anti-paralleled diode $D_{1}$ to transfer energy back to the input voltage source, which provides ZVS operation for the main switch $S_{1}$ turn-on. Gate signal of $S_{1}$ should be applied during this mode. The current $i_{L r}$ increases from negative with the slope of $V_{B} / L_{r}$. This mode ends while $i_{L}$ reaches zero.


Figure 6. Equivalent circuits of operation modes.

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Mode $9\left(\mathrm{t}_{8} \sim \mathrm{t}_{9}\right)$ : At the moment $\mathrm{t}=\mathrm{t}_{8}$, the current $i_{L r}$ becomes positive. The current $i_{D 4}$ is decreasing continuously, and this mode ends when $i_{D 4}$ reaches zero. When the diode $D_{4}$ is turned off at $t$ $=\mathrm{t}_{9}$, the operation goes to Mode 1 of the next highfrequency switching cycle.

## 3. Design procedures

Based on the operation principles presented previously, the proposed color LED driver with the specifications listed in table 1 is taken as an illustrative example. The designed procedures of the active clamp forward converter are described as follows.

Step 1-Select $f_{s}$ and $D$ : Select the switching frequency $f_{s}$ as 150 kHz . For the normal operation condition with 24 V battery voltage and full load output, the duty ratio $D$ of the main switch $S_{1}$ is determined as 0.5 .

Step 2-Determine n : Considering that the forward voltage $V_{f}$ of the diode $D_{3}$ is around 0.7 V , the minimum turn ratio ( n ) of the transformer $T_{x}$ can be determined as
$n \geq \frac{V_{B} \cdot D}{\left(V_{o}+V_{f}\right)} \cong 0.99$
In the illustrative case, $(\mathrm{n})$ is set as 1 .
Step 3-Determine $L_{0}$ : In order to minimize the output ripple current and reduce the current stress of power components, we select $10 \%$ of maximum output current as the boundary condition. The boundary inductance of $L_{o}$ can be obtained as
$L_{o B}=\frac{V_{o} \cdot(1-D)}{2 \cdot I_{o B} \cdot f_{s}} \cong 95 \mu H$
In the illustrative case, $L_{o}$ is set at $100 \mu \mathrm{H}$.
Step 4-Determine $C_{0}$ : While output inductance $L_{0}$ is determined, considering that the specification of $\Delta V_{0}$ is 10 mV , the capacitance of $C_{o}$ can be obtained
$C_{o}=\frac{(1-D)}{8 L_{o} f_{s}^{2}} \cdot\left(\frac{V_{o}}{\Delta V_{o}}\right)=31.7 \mu F$

| input voltage, $V_{B}$ | $18 \sim 36 \mathrm{~V}_{\mathrm{dc}}$ |
| :--- | :--- |
| output voltage, $V_{o}$ | $11.4 \mathrm{~V}_{\mathrm{dc}}$ |
| maximum output current, $I_{o}$ | 2 A |
| ripple of output voltage, $\Delta V_{o}$ | $10 \mathrm{mV}_{\mathrm{pp}}$ |
| switching frequency, $f_{s}$ | 150 kHz |

Table 1. Electrical Specifications of the Prototype.
The standard capacitance with $33 \mu \mathrm{~F}$ is selected for $C_{0}$.

Step 5-Determine $n_{1}$ : Select core material as TDK PC40 and core size as EE-19. Its saturation flux density $B_{\text {sat }}$ is 4500 G at $60^{\circ} \mathrm{C}$, and effective cross area $A_{e}$ is $0.23 \mathrm{~cm}^{2}$. To avoid core saturation, we set the maximum flux density $B_{\text {max }}$ at half of $B_{\text {sat }}$. Therefore, the turn number of primary winding $n_{1}$ can be calculated as
$n_{1}=\frac{V_{B} D T_{s}}{2 B_{\text {max }} A_{e}}=7.7$
In the illustrative case, $n_{1}$ is set as 8 . The inductance of $L_{m}$ can be measured as $80 \mu \mathrm{H}$.

Step 6-Determine $L_{r}$ : According to the operation principle of Mode 6 , the current $i_{L r}$ at $t=t_{6}$ can be obtained from following
$\left|L_{L r}\left(t_{6}\right)\right|=\frac{D V_{B}}{2 L_{m} f_{s}}=0.5 \mathrm{~A}$
To achieve ZVS operation of the main switch $S_{1}$, the inductance of $L_{r}$ should meet following equation

$$
\begin{equation*}
L_{r}>\frac{C_{d s} V_{B}^{2}}{\left|i_{L r}\left(t_{6}\right)\right|^{2}} \tag{10}
\end{equation*}
$$

Considering that the intrinsic capacitor $C_{d s}$ of IRF530 is 190 nF , the required inductance of $L_{r}$ can be obtained as 438 nH .

Step 7-Determine $C_{c}$ : To optimize the voltage stresses of $C_{c}, S_{1}$ and $S_{2}$, the period of the resonance occurring during $S_{1}$ turning-off should
be longer than ten times of the turn-off time of $S_{1}$, which is expressed as
$2 \pi \sqrt{\left(L_{r}+L_{m}\right) C_{c}} \geq 10 \times(1-D) / f_{s}$

Thus, the minimum capacitance of $C_{c}$ can be obtained as

$$
\begin{equation*}
C_{c} \geq \frac{100(1-D)^{2}}{\left(L_{r}+L_{m}\right)(2 \pi f)^{2}}=350 n F \tag{12}
\end{equation*}
$$

The standard capacitance with 470 nF is selected for $C_{c}$.

The calculated component parameters of the illustrative example are summarized in table 2.

## 4. Experimental results

While input voltage $V_{B}$ is 24 V , figure 7 shows the measured waveforms of the output voltage $V_{o}$ and the output inductor current $i_{L o}$ at full load operation. It can be seen that $V_{o}$ is regulated at 11.41 V , and the average output current is 2.04 A , which are very close to the required specifications. The measured waveform of clamping capacitor voltage $V_{c}$ according the driving signal $V_{G S 1}$ is shown in figure 8. It is obvious that resonance with the frequency determined by $C_{c}$ and ( $L_{r}+L_{m}$ ) occurs after the main switch $S_{1}$ is turning off. In this time interval, the resonant current $i_{L r}$ transfers energy from $L_{m}$ to $C_{c}$, and then restores energy to the input voltage source.

| main switch, $S_{1}$ | IRF530 (100V/10A) |
| :--- | :--- |
| auxiliary switch, $S_{2}$ | IRF9540 (100V/19A) |
| diodes, $D_{3}, D_{4}$ | SB1060 (60V/10A) |
| leakage inductor, $L_{r}$ | 438 nH |
| magnetizing inductor, $L_{m}$ | $80 \mu \mathrm{H}$ |
| clamping capacitor, $C_{c}$ | $0.47 \mu \mathrm{~F}$ |
| output inductor, $L_{o}$ | $100 \mu \mathrm{H}$ |
| output capacitor, $C_{o}$ | $33 \mu \mathrm{~F}$ |
| transformer turn ratio, $\mathrm{n}: 1$ | $1: 1$ |

Table 2. Component Parameters of the Prototype.


Figure 7. Measured waveforms of the output voltage $V_{o}$ and the output inductor current $i_{L o}$ at full load operation ( $V_{o}: 5 \mathrm{~V} / \mathrm{div}$; $i_{L}: 1 \mathrm{~A} / \mathrm{div}$; time: $2 \mu \mathrm{~s} / \mathrm{div}$ ).


Figure 8. Measured waveforms of clamping capacitor voltage $V_{c}$ according the driving signal $V_{G S 1}$ ( $V_{G S 1}: 10 \mathrm{~V} / \mathrm{div} ; V_{c}: 20 \mathrm{~V} / \mathrm{div}$; time: $1 \mu \mathrm{~s} / \mathrm{div}$ ).

Figure 9 shows the measured voltage and current waveforms of the main switches $S_{1}$ at full load condition. It could be observed that $S_{1}$ can turn on under ZVS condition. Figure 10 shows the measured voltage waveforms of the auxiliary switches $S_{2}$ at full load condition. The drain to source voltage $v_{d s 2}$ becomes zero before the driving signal $V_{G S 2}$ is supplied, from which the ZVS operation of $S_{2}$ can be verified. Therefore, the switching losses can be significantly minimized to improve system efficiency.

Figure 11 shows the measured efficiency curve of the proposed color LED driver with 24 V input voltage. The maximum efficiency is up to $92.5 \%$ at $I_{0}=1.6 \mathrm{~A}$. The normal operating range for driving color LED arrays is from 1.2 A to 1.8 A . The system efficiency can be kept above $91 \%$ in this range, which provide energy saving significantly.


Figure 9. Measured voltage and current waveforms of the main switch $S_{1}$ at full load operation ( $v_{d s 1}$ : $50 \mathrm{~V} / \mathrm{div}$; $i_{\text {St }}: 5 \mathrm{~A} / \mathrm{div}$; time: $\left.1 \mu \mathrm{~s} / \mathrm{div}\right)$.


Figure 10. Measured voltage waveforms of the auxiliary switch $S_{2}$ at full load operation ( $v_{d s 2}$ : $20 \mathrm{~V} / \mathrm{div}$; $v_{G S 2}: 5$ V/div; time: $1 \mu \mathrm{~s} / \mathrm{div}$ ).


Figure 11. Measured efficiency curve of the proposed color LED driver.

Figure 12 shows the measured waveforms of output voltage $V_{o}$ and output current $I_{0}$ while the LED module outputs "white" light. It can be seen
that red, green, and blue LED arrays are driving in sequential and with the frequency of 30 Hz . The output voltage $V_{o}$ is around 10.4 V for driving red LEDs, and $V_{o}$ is regulated to around 11.4 V for driving green and blue LEDs. The duty cycle of each LED is almost $100 \%$ so that all of them provide the highest luminance to have "white" light output. In figure 13, the duty cycle of green LEDs is changed to be $50 \%$ to reduce the luminance. Since the luminance of red and blue LEDs remain $100 \%$, the color of the light provided by LED modules is "purple". These experimental results verified that a large chromaticity variation can be achieved by low-frequency PWM control. By the way, the photo of the illustrative prototype is shown in figure 14.


Figure 12. Measured waveforms of output voltage $V_{o}$ and output current $I_{0}$, while the LED module outputs "white" light ( $V_{o}: 2 \mathrm{~V} / \mathrm{div}$; $I_{o}: 500 \mathrm{~mA} / \mathrm{div}$; time: $10 \mathrm{~ms} / \mathrm{div}$ ).


Figure 13. Measured waveforms of output voltage $V_{o}$ and output current $I_{0}$ while the LED module outputs "purple" light ( $V_{o}: 2 \mathrm{~V} / \mathrm{div} ; I_{0}: 500 \mathrm{~mA} / \mathrm{div}$; time: $10 \mathrm{~ms} / \mathrm{div}$ ).


Figure 14. Illustrative prototype of the proposed color LED driver.

## 5. Conclusion

An active clamp forward converter with the SCD control for driving RGB LED arrays has been proposed. ZVS turn-on operations of both the main switch and the auxiliary switch lead to low switching losses and high system conversion efficiency. RGB LED arrays are sequentially driven by the same converter, which can save components and reduce cost significantly. Besides, PWM control is applied to achieve a large chromaticity variation. Experimental results of a 100 W illustrative example have really proved the feasibility and validity of the theoretical predictions.

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